## Yu-Ju Huang

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Education	Cornell University, Ithaca, NY
	Ph.D. in Computer Science Aug. 2019 – Present
	Advisor: Prof. Robbert van Renesse
	National Chiao Tung University, Hsinchu, Taiwan
	M.S. in Computer Science, GPA: 4.0 Sep. 2013 - Jun. 2015
	Thesis: A KVM-based Hypervisor for Heterogeneous System Architecture
	Advisor: Prof. Wei-Chung Hsu
	National Chiao Tung University, Hsinchu, Taiwan
	B.S. in Computer Science, GPA: 3.91 Sep. 2009 - Jun. 2013
Destantes	Computer Systems Personacher & System Software Engineer
Professional	* Research Expertise: distributed systems databases MI systems operating systems compilers
Summary	- PhD research on designing high-performance, strongly consistent consensus protocols to
-	enhance the performance of <b>transactional databases and streaming data processing</b> .
	- As a research intern at AWS, worked on vector databases for Generative AI, streaming LLM
	inference pipelines, and <b>data lake</b> optimization.
	- Conducted research on operating systems, focusing on enabling virtualization for emerging
	hardware features, such as virtualizing unified address spaces for CPU and GPU.
	* Software Development: 3+ years of experience in industry-quality software development.
	- Developed compilers and runtimes for in-house deep learning accelerators (DLA) at a
	leading IC design company.
	* Programming Languages: Proficient in C, C++, Rust, Java, Python, and Go.
Work	Applied Scientist Intern - Amazon Web Services (AWS) Cambridge, UK   May-Aug 2024
Experience	* Vector Database for Streaming Data and GenAI/RAG
experience	- Designed an end-to-end Flink pipeline for vector search, enabling real-time data integration
	for Generative AI (GenAI) and Retrieval-Augmented Generation (RAG).
	* Vector Database for Large-Scale Data
	- Developed an ANN-based VectorDB supporting vector mutation and hosting > 1TB data.
	This internship resulted in <b>two patent</b> filings.
	Applied Scientist Intern - Amazon Web Services (AWS) Cambridge, UK   May-Aug 2023
	* Data Lake Optimization
	- Applied statistical analysis to optimize Parquet tables, reducing file sizes and improving query performance.
	* Streaming LLM Informed
	- Implemented LLM inference on Flink using either in-memory ML model or external ML agent.
	Applied Scientist Intern - Amazon Web Services (AWS) Seattle, US   May-Aug 2022

\* Transactional Key-Value Store Verification

- Built an infrastructure in Rust to verify invariants of a transaction KVS library.

	<ul> <li>System Software Engineer - MediaTek, Office of CTO Hsinchu, Taiwan   Dec 2015-Jun 2019</li> <li>* Compiler &amp; Runtime for ML Inference Frameworks <ul> <li>Developed a compiler for an in-house deep learning accelerator (DLA).</li> <li>Built frameworks for running AI models (TensorFlow, Android NN) on CPUs, GPUs, and DLAs.</li> <li>Led a taskforce to optimize DLA performance.</li> </ul> </li> <li>* Android Runtime &amp; Compiler Optimization <ul> <li>Implemented a staged compiler using LLVM to optimize Android applications</li> </ul> </li> </ul>
	<ul> <li>* QoS-Based Framework         <ul> <li>Developed a quality-of-service (QoS) framework to optimize Android runtime by dynamically adjusting system resources based on QoS hints.</li> </ul> </li> </ul>
Awards	<ul> <li>* Cornell University Fellowship, 2019-2020</li> <li>* Columbia University Presidential Fellowship (declined), 2019-2023</li> <li>* Best Paper Award, 12th International Conference on Virtual Execution Environments (VEE'16)</li> </ul>
Professional Service	<ul> <li>Shadow Program Committee Member</li> <li>18th European Conference on Computer Systems (EuroSys'23)</li> <li>Program Committee Member and Conference Session Chair</li> <li>13th International Conference on Virtual Execution Environments (VEE'17)</li> </ul>
Publications	Ziplog: A Totally Ordered Log combining Low Latency with Scalable Throughput <b>Yu-Ju Huang</b> , Shubham Chaudhary, Lorenzo Alvisi, Robbert van Renesse Under submission
	Fast Replica Coordination with ZIP <b>Yu-Ju Huang</b> , Shubham Chaudhary, Rafael Soares, Shir Cohen, Lorenzo Alvisi, Luis Rodrigues, Robbert van Renesse Under submission
	Disaggregated Applications Using Nanoservices Xinwen Wang, <b>Yu-Ju Huang</b> , Tiancheng Yuan, Robbert van Renesse Workshop On Resource Disaggregation and Serverless (WORD'21), April 2021
	Building a KVM-based Hypervisor for a Heterogeneous System Architecture Compliant System <b>Yu-Ju Huang</b> , Hsuan-Heng Wu, Yeh-Ching Chung, Wei-Chung Hsu. 12th International Conference on Virtual Execution Environments (VEE'16), April 2016 <b>Best Paper Award</b>
Teaching	<b>Cornell University</b> Head TA for CS 4411: Practicum in Operating Systems Head TA for CS 4410: Operating Systems Grad TA for CS 6410: Advanced Systems Grad TA for CS 3410: Computer System Organization and Programming